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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
DH	A1	6,252,917	6/26/01	Freeman	375	340	

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES	NO
DH	A2	2000-101453	4/7/00	Japan				Abstract
DH	A3	2000-201085	7/18/00	Japan				Abstract
DH	A4	2000-091927	3/31/00	Japan				Abstract
DH	A5	2000-078029	3/14/00	Japan				Abstract
DH	A6	11-163742	6/18/99	Japan				Abstract
DH	A7	63-045921	2/26/88	Japan				Abstract

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

DH	A8	P. Robertson et al. "A Comparison of Optimal and Sub-optimal MAP Decoding Algorithms in the Log Domain", Institute for Communications Technology, pgs. 1009-1013, (1995).
DH	A9	G. Masera et al., "VLSI Architectures for Turbo Codes", IEEE Transactions on VLSI Systems, pgs. 369-379, (1999)
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EXAMINER	DATE CONSIDERED
	01/03/05

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